

# Fast Solving of Influence Diagrams for Multiagent Planning on GPU-enabled Architectures

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Abstract: Planning under uncertainty in multiagent settings is highly intractable because of history and plan space complexities. Probabilistic graphical models exploit the structure of the problem domain to mitigate the computational burden. In this paper, we introduce the first parallelization of planning in multiagent settings on a CPU-GPU heterogeneous system. In particular, we focus on the algorithm for exactly solving *interactive dynamic influence diagrams*, which is a recognized graphical models for multiagent planning. Beyond parallelizing the standard Bayesian inference, the computation of decisions' expected utilities are parallelized. The GPU-based approach provides significant speedup on two benchmark problems.

## 1 INTRODUCTION

Planning under uncertainty in multiagent settings is a very hard problem because it involves reasoning about the actions and observations of multiple agents simultaneously. In order to formally study this problem, the approach is to generalize single-agent planning frameworks such as the partially observable Markov decision process (POMDP) (Smallwood and Sondik, 1973) to multiagent settings. This has led to the decentralized POMDP (Bernstein et al., 2005) for multiagent planning in cooperative settings and the interactive POMDP (Gmytrasiewicz and Doshi, 2005) for individual planning in cooperative or non-cooperative multiagent settings. A measure of the involved computational complexity is available by noting that the problem of solving a decentralized POMDP exactly for a finite number of steps is NEXP complete (Bernstein et al., 2002).

Some of the complexity of multiagent planning may be mitigated by exploiting the structure in the problem domain. Often, the state of the problem can be *factored* into random variables and the conditional independence between the variables may be naturally exploited by representing the planning problem using probabilistic graphical models. An example of such a model is the *interactive dynamic influence diagram* (I-DID) (Doshi et al., 2009) that generalizes the well-known DID (Howard and Matheson, 1984), which may be viewed as a graphical counterpart of POMDP, to multiagents settings in the same way that

an interactive POMDP generalizes the POMDP. In addition to modeling the problem structure, graphical models provide an intuitive language for representing the planning problem thereby serving as an important tool to enable multiagent planning.<sup>1</sup>

Emerging applications in automated vehicles that communicate (Luo et al., 2011), integration with the belief-desire-intention framework (Chen et al., 2013), and for ad hoc teamwork (Chandrasekaran et al., 2014) motivate improved solutions of I-DIDs. While techniques exist for introducing further efficiency into solving I-DIDs (Zeng and Doshi, 2012), we may also explore parallelizing its solution algorithm on new high-performance computing architectures such as those utilizing graphic processing units (GPU). A GPU consists of an array of streaming multiprocessors (SM) connected to a shared memory. Each SM typically consists of a set of streaming processors. Consequently, a GPU supplements the CPU by enabling massive parallelization of simple computations that do not require excessive memory.

Our contribution in this paper is ways of parallelizing multiple steps of the algorithm for exactly solving I-DIDs on CPU-GPU architectures. This promotes significantly faster planning on benchmark and large multiagent problems up to an order of magnitude in comparison to the run-time performance of the

<sup>1</sup>A GUI-based software application called Netus is freely available from <http://tinyurl.com/mwrtlvq> for designing I-DIDs.

existing algorithm. In addition to the usual chance, decision and utility nodes, I-DIDs include a new type of node called the model node and a new link called the policy link between the model node and a chance node that represents the distribution over the other agent's actions given its model.

The algorithm for solving an I-DID expands a given two-time slice I-DID over multiple steps and collapses the I-DID into a flat DID. We may then use the standard sum-max-sum rule and a generalized variable elimination algorithm for IDs (Koller and Friedman, 2009) to compute the maximum expected utilities of actions at each decision node to solve the I-DID. Multiple models in the model node are recursively solved in an analogous manner. Our approach is to parallelize two steps of this algorithm: (i) The four operations involved in the sum-max-sum rule: max-marginalization (of decisions), sum-marginalization (of chance variables), factor-product (of probabilities and utilities) and factor-addition (of utilities) are parallelized on the GPU. (ii) Probability factors in the variable elimination could be large joints of the Bayesian network at each time slice, and we parallelize the message passing performed on a junction tree during the inference, on the GPU.

We evaluate the parallelized I-DID solution algorithm on two benchmark planning domains, and show more than an order of magnitude in speed up on some of the problems compared to the previous algorithm. We evaluate on planning domains that in size of the state, action and observation spaces, and extend the planning over longer horizons. In addition, we study the properties of our algorithm by allocating it increasing concurrency on the GPU and show that its run time improves up to a point beyond which the gains are lost.

The rest of the paper is organized as follows. Section 2 provides preliminaries about the I-DID and concepts of GPU-based programming. Section 3 reviews related work. Section 4 proposes a GPU-based approach to exactly solve the I-DID in parallel. Section 6 theoretically analyze the speed up. Section 7 demonstrates the speed up by the proposed approach on two problems. Section 8 concludes this paper.

## 2 BACKGROUND

In this section, we briefly review the probabilistic graphical model, DID, and its generalization to multiagent settings, I-DID. General principles behind GPU-based programming are also briefly described.

### 2.1 Dynamic Influence Diagram

A DID,  $\mathcal{D}$ , is a directed acyclic graph over a set of nodes: chance nodes  $\mathbf{C}$  (ellipses), representing random variables; decision nodes  $\mathbf{D}$  (rectangles), modeling the action choices; utility nodes  $\mathbf{U}$  (diamonds), representing rewards based on chance and decision node values, and a set of arcs representing dependencies. Conditional probability distributions,  $\mathbf{P}$ , and utility functions,  $\mathbf{R}$ , are associated with the chance and utility nodes, respectively. In rest of the paper, nodes and variables are used interchangeably.

The domain of a variable  $Q$ , denoted as  $dom(Q)$ , contains its possible values. The parent of  $Q$ , denoted as  $Pa_Q$ , is a set of variables having direct arcs incident on  $Q$ . The domain of  $Pa_Q$ ,  $dom(Pa_Q)$ , is the Cartesian product of the individual domains:  $dom(Pa_Q) = \prod_{Z \in Pa_Q} dom(Z)$ , and a value of this domain is denoted as,  $pa_Q$ . A probability factor,  $\phi(Q) = P(Q|Pa_Q)$ , which defines conditional probability distribution given instantiation of parent variables, is attached to each chance variable  $Q \in \mathbf{C}$ . We use  $Ch_Q$  to denote  $Q$ 's children. A utility factor,  $\psi(U) = R(Pa_U)$ , where  $R$  returns real-valued rewards, is associated with each utility node,  $U \in \mathbf{U}$ . The variables involved in a probability or utility factor become the domain of this factor, for example,  $dom(\phi(Q)) = \{Q\} \cup Pa_Q$ .

A policy for decision node,  $D_i \in \mathbf{D}$ , is a mapping,  $\delta_i : dom(Pa_{D_i}) \rightarrow dom(D_i)$ , i.e.,  $\delta_i(pa_{D_i}) = d_i$ . A policy for the decision problem is a sequence of policies for all the decision nodes. The solution of a DID is a strategy that maximizes the expected value  $MEU(\mathcal{D})$ , computed using the *sum-max-sum rule* (Koller and Friedman, 2009):

$$\sum_{I_0} \max_{D_1} \sum_{I_1} \dots \max_{D_n} \sum_{I_n} \left( \prod_{Q_i \in \mathbf{C}} P(Q_i|Pa_{Q_i}) \cdot \sum_{\mathbf{C}, \mathbf{D}} R(\mathbf{C}, \mathbf{D}) \right)$$

where  $I_0, I_1, \dots, I_{n-1}$  is the set of chance variables incident on the decision nodes,  $D_1, D_2, \dots, D_n$ , thereby forming the information sets.

The MEU may be computed by repeatedly eliminating variables. Let  $\Phi$  and  $\Psi$  be the set of probability and utility factors, respectively. Given variable  $Q$ , the probability and utility factors having  $Q$  in their domain are denoted as  $\Phi_Q$  and  $\Psi_Q$ , respectively. After  $Q$  is eliminated, the factor sets are updated as follows:

$$\Phi = (\Phi \setminus \Phi_Q) \cup \{\phi_{\setminus Q}\} \text{ and } \Psi = (\Psi \setminus \Psi_Q) \cup \left\{ \frac{\psi_{\setminus Q}}{\phi_{\setminus Q}} \right\}.$$

Here,  $\phi_{\setminus Q} = \sum_Q \prod \Phi_Q$  and  $\psi_{\setminus Q} = \sum_Q \prod \Phi_Q (\sum \Psi_Q)$  when  $Q$  is a chance variable; if  $Q$  is a decision variable, then,  $\phi_{\setminus Q} = \max_Q \prod \Phi_Q$  and  $\psi_{\setminus Q} = \max_Q \prod \Phi_Q (\sum \Psi_Q)$ .

## 2.2 Interactive DID

Interactive DID (I-DID) (Doshi et al., 2009) models an individual agent’s planning (sequential decision making) in a multiagent setting. In a I-DID, other agents’ candidate behaviors are modeled as they impact the common states and rewards during the subject agent’s decision-making process. Simultaneously, other agents also reason about the subject agent’s possible actions in their decision making. This recursive modeling is encoded in an auxiliary data item called the model node  $M_{j,l-1}^t$  which contains models of the other agent, say  $j$  of level  $l-1$  and chance node  $A_j$  which represents the distribution over  $j$ ’s actions. The link between  $M_{j,l-1}^t$  and  $A_j^t$ , named as policy link, indicates that the other agent’s predicted action is based on its models. The models can be DIDs, I-DIDs or simply probability distributions over actions. The link between  $M_{j,l-1}^t$  and  $M_{j,l-1}^{t+1}$ , called model update link, represents the update of  $j$ ’s model over time.

**Example 1** (Multiagent tiger problem (Gmytrasiewicz and Doshi, 2005)). Consider two agents standing in front of two closed doors with a tiger or some gold behind each door. If an agent opens a door with a tiger behind it, it receives a penalty, otherwise a reward. Agents can listen for growls as well as hear creaks if the other agent opens a door. But, listening is not accurate. When the agent receives a reward or penalty, the game is reset. There is another agent  $j$  with the same character sharing the environment with agent  $i$  without noticing the existence of agent  $i$ . They receive reward or penalty together, therefore agent  $i$  needs to take into account agent  $j$ ’s behavior. A two time-slice I-DID for agent  $i$  situated in the multiagent tiger problem is depicted in Fig. 1.

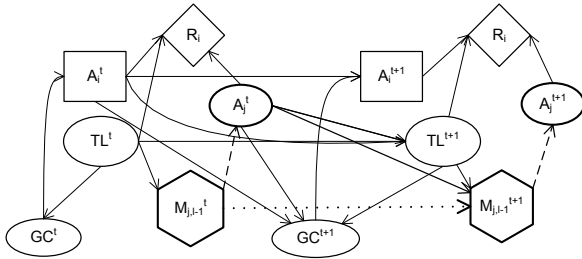


Figure 1: A two time-slice I-DID for agent  $i$  in the tiger problem. Policy links are marked as dash lines, while model update links are marked as dotted lines.  $TL$  stands for ‘Tiger Location’ and  $GC$  stands for ‘Growl&Creak’.

Solving an I-DID (shown in Fig. 3) requires solving the lower-level models, and this recursive procedure ends at level 0 where the I-DID reduces to a

DID (Line 4). The policies from solving lower-level models are used to expand the next higher-level I-DID (Line 5 - 10). We may then replace the model nodes, policy and the model update links with regular chance nodes and dependency links. States of the nodes and parameters of the links are specified according to the obtained policies (Line 11 - 13). Subsequently, an I-DID becomes a regular DID, whose MEU is obtained (Line 15). Doshi and Zeng (2009) provide more details about I-DIDs including an algorithm for solving it optimally.

**Example 2.** The I-DID shown in Fig. 1 is expanded as shown in Fig. 2.  $GC$  denotes a chance variable for observations of Growl&Creak, and the remaining chance nodes are grouped and denoted by  $\mathbf{X}_i$  for convenience. The MEU is calculated as follows.

$$MEU[\mathcal{D}] = \sum_{\mathbf{X}_i} \max_{A_i} \sum_{GC^t} P(\mathbf{X}_i^t) P(GC^t | \mathbf{X}_i^t) \sum_{\mathbf{X}_i^{t+1}} \max_{A_i^{t+1}} \sum_{GC^{t+1}} P(\mathbf{X}_i^{t+1} | \mathbf{X}_i^t, A_i^t) P(GC^{t+1} | \mathbf{X}_i^{t+1}) [R_i^t(A_i^t, \mathbf{X}_i^t) + R_i^{t+1}(A_i^{t+1}, \mathbf{X}_i^{t+1})] \quad (1)$$

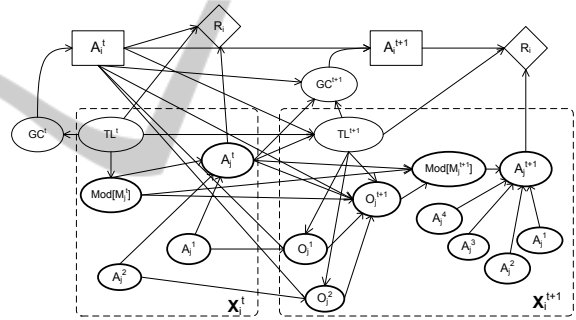


Figure 2: The flat two time-slice DIDs for the tiger problem. Model nodes are replaced by a set of ordinary chance nodes. All hidden variables are grouped as  $\mathbf{X}_i$ .

## 2.3 GPU and CUDA

Graphics processing units (GPUs) were originally designed for rendering computer graphics.

In a GPU, there are a number of streaming multiprocessors (SM), each containing a set of stream processors, registers and shared local memory (SMEM). At run time, a set of parallelized computation tasks referred to as a thread block are executed on a SM and distributed across the processors. In order to achieve good performance, it is crucial to map algorithms to the GPU architecture efficiently, which is optimized for high throughput. For example, designs that favor coalesced memory access are cost-effective. In the past decade, general purpose computing on the GPU has increased with a focus on bridging the gap between GPUs and CPUs by letting GPUs handle the

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I-DID EXACT
(level  $l \geq 1$  I-DID or level 0 DID, horizon  $T$ )
Expansion Phase
1. For  $t$  from 0 to  $T - 1$  do
2.   If  $l \geq 1$  then
     Populate  $M_{j,l-1}^{t+1}$ 
3.   For each  $m_j^t$  in  $\text{Range}(M_{j,l-1}^t)$  do
4.     Recursively call algorithm with the  $l - 1$ 
       I-DID (or DID) that represents  $m_j^t$  and
       the horizon,  $T - t$ 
5.     Map the decision node of the solved I-DID
       (or DID),  $OPT(m_j^t)$ , to the corresponding
       chance node  $A_j$ 
6.     For each  $a_j$  in  $OPT(m_j^t)$  do
7.       For each  $o_j$  in  $O_j$  (part of  $m_j^t$ ) do
8.         Update  $j$ 's belief,
            $b_j^{t+1} \leftarrow SE(b_j^t, a_j, o_j)$ 
9.          $m_j^{t+1} \leftarrow$  New I-DID (or DID) with
            $b_j^{t+1}$  as the initial belief
10.         $\text{Range}(M_{j,l-1}^{t+1}) \leftarrow \bigcup \{m_j^{t+1}\}$ 
11.        Add the model node,  $M_{j,l-1}^{t+1}$ , and the model update
           link between  $M_{j,l-1}^t$  and  $M_{j,l-1}^{t+1}$ 
12.        Add the chance, decision, and utility nodes for  $t + 1$ 
           time slice and the dependency links between them
13.        Establish the CPDs for each node
Solution Phase
14. If  $l \geq 1$  then
15.   Represent the model nodes, policy links and the
       model update links as in Fig. 1 to obtain the DID
16.   Apply the standard sum-max-sum rule to solve the
       expanded DID (other solution approaches may also be used)

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Figure 3: Algorithm for exactly solving a level  $l \geq 1$  I-DID or level 0 DID expanded over  $T$  time steps.

most intensive computing while still leaving controlling tasks to CPU. CUDA provided by NVIDIA is a general-purpose parallel computing programming model for NVIDIA's GPUs. CUDA abstracts most operational details of GPU and alleviates the developer from the technical burden GPU-oriented programming. An important component of a CUDA program is a *kernel*, which is a function that executes in parallel on a thread block.

### 3 RELATED WORK

Multiple frameworks formalize planning under uncertainty in settings shared with other agents who may have similar or conflicting objectives. A recognized framework in this regard is the interactive POMDP (Gmytrasiewicz and Doshi, 2005) that facilitates the study of planning in partially observable multiagent settings where other agents may be cooperative or non-cooperative. I-DIDs (Doshi et al., 2009) are a graphical counterpart of interactive

POMDPs and have the advantage of a representation that explicates the embedded domain structure by decomposing the state space into variables and relationships between the variables.

I-DIDs contribute to a promising line of research on graphical models for multiagent decision making and planning, which includes multiagent influence diagrams (MAID) (Koller and Milch, 2001), networks of influence diagrams (NID) (Gal and Pfeffer, 2008), and limited memory influence diagram based players (Søndberg-Jeppesen et al., 2013). I-DIDs differ from MAIDs and NIDs by offering a subjective perspective to the interaction and solutions not limited to equilibria, by ascribing other agents with a distribution of non-equilibrium behaviors as well. Importantly, I-DIDs offer solutions over extended time interactions, where agents act and update their beliefs over others' models which are themselves dynamic.

Previous uses of CPU-GPU heterogeneous systems in the context of graphical models focus on speeding up exact inference in Bayesian networks due to parallelization (V. Kozlov and Pal Singh, 1994; Jeon et al., 2010; Xia and Prasanna, 2008). For example, Jeon et al. (2010) report speedup factors in the range from 5 to 12 for both marginal and most probable inference in junction trees. *In comparison, we elevate the problem from performing inference in junction trees to finding optimal policies in I-DIDs and DIDs.* As solving I-DIDs requires performing inference on the underlying Bayesian network in each time slice, our approach also parallelizes exact inference using junction trees in a manner similar to previous work (Zheng et al., 2011). Additionally, we provide a fast method for evaluating the sum-max-sum rule for DIDs by parallelizing component operations such as sum-marginalization and others on a GPU.

## 4 PARALLELIZED I-DID EXACT FOR CPU-GPU SYSTEMS

Our approach revises the algorithm, I-DID Exact, presented in Fig. 3 by parallelizing two component steps for utilization on a CPU-GPU heterogeneous computing architecture and through leveraging some of the recent advances in parallelizing inference in Bayesian networks.

### 4.1 Parallelizing Sum-Max-Sum Rule for MEU

A solution of the sum-max-sum rule mentioned in Section 2 gives the maximum expected utility of the

flat DID that results from transforming the I-DID. The temporal structure of the DID provides an ordering of the chance, decision and utility variables that is utilized by generalized variable elimination for IDs to compute the MEU. In our two-time slice DID for the multiagent tiger problem, the elimination ordering is:  $\mathbf{X}^{t+1}, \mathbf{Y}^{t+1}, A_i^{t+1}, \mathbf{X}^t, \mathbf{Y}^t, A_i^t$ , where  $\mathbf{X}$  and  $\mathbf{Y}$  are the sets of hidden variables and those in the information set of a decision variable in each time slice, respectively. The sum-max-sum rule does not specify an ordering between the variables  $\mathbf{X}$  and  $\mathbf{Y}$ .

#### 4.1.1 Memory-efficient Variable Elimination for DIDs

In order to efficiently use the CPU-GPU memory, we design the variable elimination memory efficiently. Specifically, instead of keeping the entire DID in memory while performing variable elimination, we lazily bring the minimal set of the other variables and their factors that are needed in order to eliminate the variable in question. We refer to this set of variables as a *cover set*. We first revisit the definition of a Markov blanket of a variable.

**Definition 1** (Markov blanket, Pearl (1998)). *The Markov blanket of a random variable  $Q$ , denoted as  $MB(Q)$ , is the minimal set of variables that makes  $Q$  conditionally independent of all other variables given  $MB(Q)$ . Formally,  $Q$  is conditionally independent of all other variables in the network given its parents, children, and children's parents.*

**Definition 2** (Cover set). *The cover set of a random variable,  $Q$ , denoted by  $CS(Q)$  is defined as:*

$$CS(Q) = \{Q\} \cup MB(Q).$$

Notice that the cover set of  $Q$  consists of itself and its Markov blanket. Furthermore, we make the following straightforward observation:

**Observation 1.**  $CS(Q)$  is exactly identical to the union of the domains of the factor of  $Q$  and the factors of the children of  $Q$ ,

$$CS(Q) = dom(\phi_Q) \bigcup_{Z \in Ch_Q} dom(\phi_Z)$$

Let  $\mathbf{X}$  be the set of variables in the elimination order that precedes  $Q$ . As the cover sets of variables in  $\mathbf{X}$  would be in memory already, we define an *incremental cover set* below that is the set of all variables in the cover set less all those variables contained in the cover sets of the variables preceding  $Q$  in the elimination ordering.

**Definition 3** (Incremental cover set). *The incremental cover set of a random variable,  $Q$ , denoted by  $ICS(Q)$  is defined as:*

$$ICS(Q) = \{Q\} \cup MB(Q) \setminus \bigcup_{X \in \mathbf{X}} CS(X),$$

where  $\mathbf{X}$  are the variables that preceded  $Q$  in the elimination ordering.

Factors related to variables in  $ICS(Q)$  need to be additionally fetched into memory because the latter cover sets are already in memory and overlapping variables need not be fetched. Lemma 1 provides a simple way to determine the incremental cover set.

**Lemma 1.** *As variable elimination proceeds, let  $\mathcal{F}_Q$  be the set of all factors not previously loaded in memory with  $Q$  in each of their domains. Then, the union of all variables in the domains of  $\mathcal{F}_Q$ , denoted as  $\Delta_Q$  forms the incremental cover set of  $Q$ .*

*Proof.* For the base case, let  $Q$  be the first variable to be eliminated. The union of domains of all factors with  $Q$  in their domains is:  $\Delta_Q = dom(\phi_1(\mathbf{X}_1)) \cup dom(\phi_2(\mathbf{X}_2)) \cup \dots \cup dom(\phi_n(\mathbf{X}_n))$ . We will show that  $\forall y \in \mathbf{X}_i, y \in MB(Q)$  or  $y = Q$  for  $i \in [1, n]$ . Suppose that  $\exists y \in \mathbf{X}_i$  and  $y \notin MB(Q)$  and  $y \neq Q$ . Given the definition of the Markov blanket,  $y$  is not a child of  $Q$  or parent of a child of  $Q$ . Therefore, from Observation 1, the corresponding factor,  $\phi_i$ , cannot contain  $Q$  in its domain. This is a contradiction and no such  $y$  exists. Therefore,  $\forall y \in \mathbf{X}_i, y \in MB(Q)$  or  $y = Q$ .

Let  $Q_k$  be the  $k^{\text{th}}$  variable to be eliminated. As the inductive hypothesis,  $\Delta_{Q_k} = \{Q_k\} \cup MB(Q_k) \setminus \bigcup_{X \in \mathbf{X}} CS(X)$ . For the inductive step, let  $Q_{k+1}$  be the next variable to be eliminated. Notice that

$$\Delta_{Q_k} = \Delta_{Q_{k+1}} \cup CS(Q_k) \cup dom(\Phi_{Q_k \setminus Q_{k+1}}) \setminus dom(\Phi_{Q_{k+1} \setminus Q_k})$$

where  $\Phi_{Q_k \setminus Q_{k+1}}$  are the factors with  $Q_k$  in their domains and not  $Q_{k+1}$  – these would be absent from  $\Delta_{Q_{k+1}}$  – and  $\Phi_{Q_{k+1} \setminus Q_k}$  are the factors with  $Q_{k+1}$  and not  $Q_k$  in their domains.

We may rewrite the above as:

$$\begin{aligned} \Delta_{Q_{k+1}} &= \Delta_{Q_k} \cup dom(\Phi_{Q_{k+1} \setminus Q_k}) \setminus dom(\Phi_{Q_k \setminus Q_{k+1}}) \setminus CS(Q_k) \\ &= dom(\Phi_{Q_{k+1} \setminus Q_k}) \cup \Delta_{Q_k} \setminus dom(\Phi_{Q_k \setminus Q_{k+1}}) \setminus CS(Q_k) \end{aligned}$$

As  $\Delta_{Q_k}$  denotes the domains of all factors with  $Q_k$  and additionally, with  $Q_{k+1}$  being present or absent,  $\Delta_{Q_k} = dom(\Phi_{Q_k, Q_{k+1}}) \cup dom(\Phi_{Q_k \setminus Q_{k+1}}) \setminus \bigcup_{X \in \mathbf{X}} CS(X)$ .

Using this in the above equation,

$$\begin{aligned} \Delta_{Q_{k+1}} &= dom(\Phi_{Q_{k+1} \setminus Q_k}) \cup dom(\Phi_{Q_k, Q_{k+1}}) \\ &\quad \cup dom(\Phi_{Q_k \setminus Q_{k+1}}) \setminus dom(\Phi_{Q_k \setminus Q_{k+1}}) \\ &\quad \setminus \bigcup_{X \in \mathbf{X}} CS(X) \setminus CS(Q_k) \\ &= dom(\Phi_{Q_{k+1} \setminus Q_k}) \cup dom(\Phi_{Q_k, Q_{k+1}}) \\ &\quad \setminus \bigcup_{X \in \mathbf{X}} CS(X) \setminus CS(Q_k) \\ &= dom(\Phi_{Q_{k+1}}) \setminus \bigcup_{X \in \mathbf{X} \cup Q_k} CS(X) \end{aligned}$$

We may apply a proof similar to that in the base case to the first term above. Therefore,

$$\begin{aligned} \Delta_{Q_{k+1}} &= \{Q_{k+1}\} \cup MB(Q_{k+1}) \setminus \bigcup_{X \in \mathbf{X} \cup Q_k} CS(X) \\ &= ICS(Q_{k+1}) \end{aligned}$$

□

Next, we establish the benefits and correctness of solely considering the cover set of  $Q$  in Theorem 1. We define the joint probability distribution of the variables in the cover set first.

**Definition 4** (Factored joint probability distribution of cover set). *The factored joint probability distribution for a cover set of a random variable,  $Q$ , is defined as:*

$$P(Q|Pa_Q) \prod_{Z \in Ch_Q} P(Z|Pa_Z)$$

**Theorem 1.** *Let  $\Phi_Q$  ( $\Psi_Q$ ) be a set of relevant probability (or utility) factors required to compute the new factor  $\phi_Q$  ( $\psi_Q$ ) for eliminating variable  $Q$ . All the variables in the domain of  $\Phi_Q$  ( $\Psi_Q$ ) exactly comprise the cover set of  $Q$ ,  $CS(Q)$ .*

*Proof.* The set of relevant probability factors  $\Phi_Q$  can be separated into two categories:  $P(Q|Pa_Q)$  and  $P(X|Pa_X)$  where  $X \in Ch_Q$ . Consequently, the variable in domains of factors in  $\Phi_Q$  are included in  $Pa_Q \cup Ch_Q \cup_{Z \in Ch_Q} Pa_Z \cup \{Q\}$ , which is the cover set of  $Q$  by definition.

Assume there exists a variable  $Y \neq Q$ ,  $Y \in CS(Q)$  and  $Y$  does not appear in either  $P(Q|Pa_Q)$  or  $P(X|Pa_X)$  where  $X \in Ch_Q$ . In other words,  $Y \notin Pa_Q$ ,  $Y \notin Ch_Q$  and  $Y \notin \cup_{Z \in Ch_Q} Pa_Z$ . Consequently,  $Y \notin MB(Q)$ . As  $Y \neq Q$ , therefore  $Y \notin CS(Q)$ , but this is a contradiction. Therefore, all variables in  $CS(Q)$  appear in the relevant factors. A similar argument is applicable to the utility factors  $\Psi_Q$ .  $\square$

Thus, the cover set of a variable,  $Q$ , locally identifies those variables whose factors change on eliminating  $Q$ . These factors contain  $Q$  in their domains. The alternative is a naive global method that searches over all factors and identifies those with  $Q$  in their domains. We illustrate the use of the cover set in eliminating chance and decision variables in the context of the multiagent tiger problem below.

**Example 3** (Variable elimination using cover set). *The two-time slice flat DID is shown in Fig. 4(a). For clarity, the hidden chance variables in each time slice are replaced with  $\mathbf{X}_i$  thereby compacting the DID. The MEU for the DID is given by Equation 1. The temporal structure of the DID induces a partial ordering for the elimination of the variables in the rule above. In the context of Fig. 4(a), this ordering is:  $\mathbf{X}_i^{t+1}$ ,  $A_i^{t+1}$ ,  $GC^{t+1}$ ,  $\mathbf{X}_i^t$ ,  $A_i^t$ ,  $GC^t$ .*

We begin by eliminating  $\mathbf{X}_i^{t+1}$  from the DID. Theorem 1 allows us to focus on the cover set of  $\mathbf{X}_i^{t+1}$  only, which is shown in Fig. 4(b).

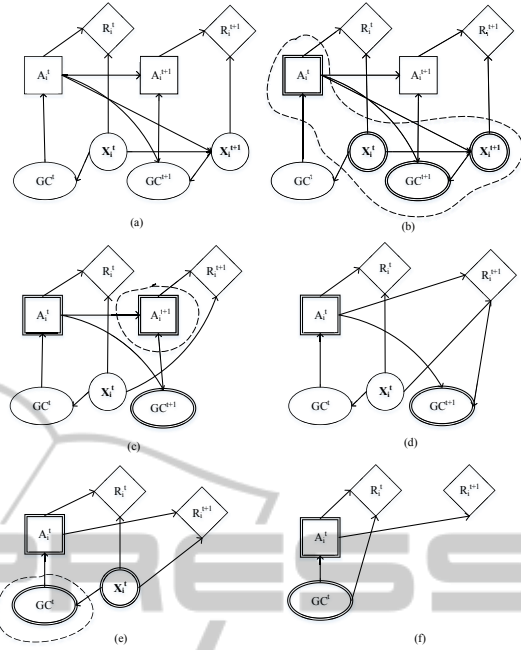


Figure 4: An illustration of variable elimination for DIDs. The incremental cover set for each variable is marked using a dashed line. In (a – f), the DID is progressively reduced following the elimination order:  $\{\mathbf{X}_i^{t+1}, A_i^{t+1}, GC^{t+1}, \mathbf{X}_i^t\}$ .

$$\begin{aligned} CS(\mathbf{X}_i^{t+1}) &\leftarrow \{\mathbf{X}_i^{t+1}\} \cup MB(\mathbf{X}_i^{t+1}) \\ &\leftarrow \{\mathbf{X}_i^{t+1}, GC^{t+1}, \mathbf{X}_i^t, A_i^t\} \\ \psi_1(GC^{t+1}, \mathbf{X}_i^t, A_i^t, A_i^{t+1}) &= \sum_{\mathbf{X}_i^{t+1}} P(CS(\mathbf{X}_i^{t+1})) R_i^{t+1}(A_i^{t+1}, \mathbf{X}_i^{t+1}) \\ &= \sum_{\mathbf{X}_i^{t+1}} P(\mathbf{X}_i^{t+1}, GC^{t+1} | \mathbf{X}_i^t, A_i^t) \times R_i^{t+1}(A_i^{t+1}, \mathbf{X}_i^{t+1}) \end{aligned}$$

Decision variable,  $A_i^t$ , in the probability factor is converted into a random variable with a uniform distribution over its states. We update the set of all utility factors as:  $\Psi \leftarrow \{\psi_1(GC^{t+1}, \mathbf{X}_i^t, A_i^t, A_i^{t+1})\}$

Next, we eliminate  $A_i^{t+1}$  from the reduced DID. Figure 4(c) shows the incremental cover set of  $A_i^{t+1}$  with the dashed loop:  $A_i^{t+1}$  and its factors additionally need to be fetched into memory.

$$\begin{aligned} CS(A_i^{t+1}) &\leftarrow \{A_i^{t+1}, GC^{t+1}, A_i^t\} \\ \psi_2(GC^{t+1}, \mathbf{X}_i^t, A_i^t) &= \max_{A_i^{t+1}} \psi_1(GC^{t+1}, \mathbf{X}_i^t, A_i^t, A_i^{t+1}) \end{aligned}$$

The set of utility factors updates to  $\Psi \leftarrow \{\psi_2(GC^{t+1}, \mathbf{X}_i^t, A_i^t)\}$ .

The DID reduces to the one shown in Fig. 4(d), from which we now eliminate  $GC^{t+1}$ . The incremental cover set of this variable is empty as all the variables in its cover set were utilized previously and preexist in memory.

$$CS(GC^{t+1}) \leftarrow \{GC^{t+1}, A_i^t\}$$

$$\psi_3(\mathbf{X}_i^t, A_i^t) = \sum_{GC^{t+1}} P(GC^{t+1} | A_i^t) \psi_2(GC^{t+1}, \mathbf{X}_i^t, A_i^t)$$

The set of utility factors now becomes:  $\Psi \leftarrow \{\psi_3(\mathbf{X}_i^t, A_i^t)\}$ .

Finally, we eliminate  $\mathbf{X}_i^t$  and  $GC^t$  after fetching  $GC^t$  (and its factors) into memory.

$$CS(\mathbf{X}_i^t) \leftarrow \{\mathbf{X}_i^t, GC^t\}$$

$$\psi_4(A_i^t, GC^t) = \sum_{\mathbf{X}_i^t} P(GC^t | \mathbf{X}_i^t) [R_i^t(\mathbf{X}_i^t, A_i^t) + \psi_3(\mathbf{X}_i^t, A_i^t)]$$

The utility factor set becomes  $\Psi \leftarrow \{\psi_4(A_i^t, GC^t)\}$ .

Maximizing over  $A_i^t$  and sum marginalization of  $GC^t$  will yield an empty factor set and the decision that maximizes the expected utility of the DID.

#### 4.1.2 Speeding Up Factor Operations using GPU

We perform the product operation between probability and utility factors in parallel on a GPU. The operation is a pointwise product of the entries in factors. When there are common variables, only entries with the same value of the common variables is multiplied. For convenience, we denote  $R_i^t(\mathbf{X}_i^t, A_i^t) + \psi_3(\mathbf{X}_i^t, A_i^t)$  simply as  $\psi_3'(\mathbf{X}_i^t, A_i^t)$ .

In order to parallelize the factor product, indices of entries to be multiplied in the factors are needed. Previous parallelization of inference in Bayesian networks sought to minimize the size of the index mapping table for GPUs (Jeon et al., 2010) due to the SM memory limitation. The entire mapping table was decomposed into smaller ones each giving the mapped indices of the entries in the second factor for each non-common variable in the first factor. Our utility factor product follows the similar principle of message passing for belief propagation in junction trees.

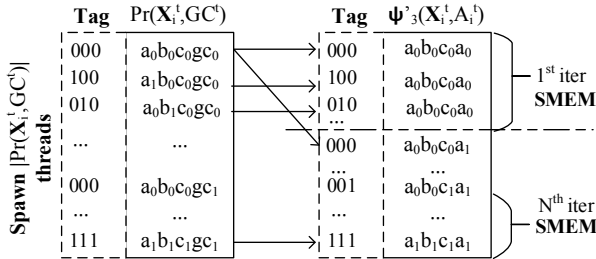


Figure 5: The index mapping table. We assume that all variables are boolean. SMEM denotes shared memory.

Entries in a factor are indexed according to variables as  $index = \sum_{Q \in dom(\psi)} state_Q \times stride_Q$ . The stride of a variable  $X_i$  in a factor,  $P(X_0, \dots, X_n)$  is defined as  $stride_{X_0} = 1$  and

$stride_{X_i} = stride_{X_{i-1}} \cdot |dom(X_{i-1})|$ , for  $i \in [1, n]$ . We also define an entry's state vector as  $\langle state_1, \dots, state_n \rangle$ . Here,  $n = |dom(\mathbf{X}_i^t)| |dom(GC^t)|$ , and  $state_Q = \lfloor \frac{index}{stride_Q} \rfloor \bmod |dom(Q)|$ . A tag for an entry is the portion of the state vector pertaining to common variables.

A thread in a SM is allocated to finding the entries of the second factor with which we may multiply a probability value in the first factor as we show in Fig. 5. We allocate as many threads as the number of distinct entries in the first factor until no more threads are available, in which case multiple entries may be assigned to the same thread. Indices for the entries whose tags match the tag of the subject entry in the first factor are obtained and the corresponding products are performed. Because the index is needed repeatedly, it is beneficial to investigate efficient ways of computing it. Notice that the *index* values can be computed as:  $index = \sum_{Q \in c.v.} state_Q \times stride_Q + \sum_{Q \in dom(\psi)/c.v.} state_Q \times stride_Q$ , here *c.v.* stands for common variables.

As a particular thread must find entries with the same tag, we compute the first summation in the above equation once, cache it and then reuse it in finding the indices of the other entries. As illustrated in Fig. 5, each thread saves on computing the first summation two times because the noncommon variable,  $A_i^t$ , has three states, thereby saving  $O(|\mathbf{X}_i^t|)$  each time which gets substantial in the context of factor products that have a large number of common variables.

Factor products in the sum-max-sum rule are usually followed by sum-marginalization operations. For example, the last variable elimination shown in Fig. 4 marginalizes the set of variables in  $\mathbf{X}_i^t$  that includes  $tiger.location^t, A_j^t, Mod[M_j^t]$ , among others, from the factor,  $P(\mathbf{X}_i^t, GC^t) \times \psi_3'(\mathbf{X}_i^t, A_i^t)$ . Let us denote the resulting product factor as,  $\psi_{34}(\mathbf{X}_i^t, GC^t, A_i^t)$ . For illustration purposes, let us focus on marginalizing a single variable,  $A_j^t \in \mathbf{X}_i^t$  from  $\psi_{34}(\mathbf{X}_i^t, GC^t, A_i^t)$ .

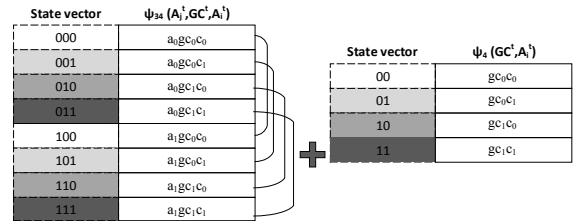


Figure 6: Four threads are used to produce the entries in the four rows of the resulting factor,  $\psi_4$ , on the right.

We parallelize and speed up sum-marginalization by allowing a separate thread to sum those entries in the factor that correspond to the different values of  $A_j^t$  while keeping the other variable values fixed (Zheng

et al., 2011) (Fig. 6).

### 4.1.3 Parallelizing Message Passing in the BN

Probability factors utilized during variable elimination for computing the MEU of the flat DID often involve joint probability distributions. For example, the factor  $P(\mathbf{X}_i^t, GC^t)$  utilized in the elimination of  $\mathbf{X}_i^t$  is the joint distribution over the multiple variables in  $\mathbf{X}_i^t$  and  $GC^t$ . We may efficiently compute the probability factor tables by forming a junction tree of the Bayesian network in each time slice, and computing the joints using message passing (Zheng et al., 2011).

Analogously to the operations involved in variable elimination, message passing in a junction tree involves sum-marginalization and factor products. However, the typical order of these operations in message passing is the reverse of those in the sum-max-sum rule: we perform marginalizations first followed by factor products. These operations are part of the marginalization and scattering steps that constitute message passing.

We parallelize message passing in junction trees to efficiently compute the probability factors. Both sum-marginalizations and factor products are performed on a CPU-GPU heterogeneous system by utilizing multiple threads in a SM each of which computes the relevant index mapping tables *online* and performs the products as we described previously in Figs. 5 and 6. This is similar to the approach of Zheng et al. (2011) that decomposes the whole index mapping table into smaller components that are relevant to each thread. However, the latter precomputes tables while forming the junction trees and stores them in memory.

## 5 DESIGN AND ALGORITHMS

The MEU of a flat DID is computed using the sum-max-sum rule. Factor product and sum-marginalization operations are parallelized by wrapping them in a GPU kernel function. This launches one or more blocks of threads for performing the products and sums of probabilities and utilities.

For the message passing performed on the junction tree, a CPU routine call selects the relevant cliques, which are nodes in the junction tree, for processing. It computes the required parameters for cliques involved in the current communication, and asynchronously transmits the result to the GPU. After all parameters are computed, a GPU block of threads is launched to compute and propagate the message to a recipient clique.

Before running the algorithm, CUDA requires the

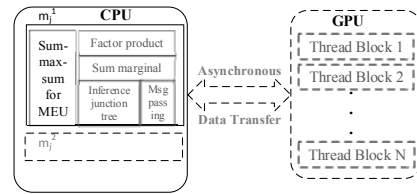


Figure 7: An abstract view of the parallelization of MEU computation for solving an I-DID on a CPU-GPU system.

kernel to be appropriately configured: in terms of grid size and shape, shared memory and registers utilization. We note three choices: 1) fixing thread block size in order to utilize more registers; 2) minimizing the number of registers to possibly achieve high occupancy; and 3) finding shared memory size per block to minimize global memory accesses. Quick experimentation revealed that for both the factor operations and the message passing algorithm, fixing the number of registers to 32 and using shared memory chunk size of 512 were suitable. For effective allocation of memory, we allocate large chunks of memory at program start, and all GPU memory allocation requests use one of these chunks of memory. If more memory is requested than chunks available, a chunk is reallocated to possibly accommodate the request.

Algorithms 1 and 2 provide the steps for performing the factor product and the sum-marginalization, respectively, on the GPU. In algorithms 1, the utility factor is divided and loaded into shade memory. The input and output indices in both algorithms are computed following the discussion in Section 4.1.2. We show the abstract design of the algorithm in Fig. 7.

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#### Algorithm 1: Factor Product on GPU.

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**Require:** probability factor  $\phi$  and utility factor  $\psi$   
**Ensure:** product factor  $\psi'$

- 1:  $tid$  is the thread id
- 2:  $numIter$  is the number of iterations
- 3:  $workSize$  is the number of products per thread
- 4: **for**  $i \leftarrow 1$  to  $numIter$  **parallel do**
- 5:    $begLoadIdx \leftarrow$  begin offset
- 6:    $endLoadIdx \leftarrow$  end offset
- 7:    $SMEM \leftarrow \psi[begLoadIdx \dots endLoadIdx]$
- 8:   **for**  $j \leftarrow 1$  to  $workSize$  **parallel do**
- 9:      $iidx$  is the input index
- 10:     $oidx$  is the output index
- 11:     $\psi'[oidx] \leftarrow \phi[tid] * SMEM[iidx]$
- 12:    **end for**
- 13: **end for**

---

## 6 ANALYSIS OF SPEED UP

We theoretically analyze the speed up resulting from parallelizing the factor product, sum-marginalization



**Algorithm 2:** Sum-marginalization on GPU.**Require:**  $\psi$  which needs to be marginalized**Ensure:** the resulting factor  $\psi'$ 

```

1:  $iid$  is the thread id
2:  $workSize$  is the number of additions per thread
3:  $sum \leftarrow 0$ 
4: for  $j \leftarrow 1$  to  $workSize$  parallel do
5:    $iidx \leftarrow$  index to  $\psi$ 
6:    $sum \leftarrow sum + \psi[iidx]$ 
7: end for
8:  $oidx \leftarrow$  output index to  $\psi'$ 
9:  $\psi'[oidx] \leftarrow sum$ 

```

and factor sum operations that are involved in computing the MEU. Let  $\phi_Q$  and  $\psi_Q$  be some probability and utility factors involving chance variable,  $Q$ , respectively, and  $S_{\phi_Q\psi_Q}$  denote the set of variables in common between the domains of the two factors. Then,  $dom(\psi_Q) - S_{\phi_Q\psi_Q}$  is the set of variables in  $\psi$  that are not in  $\phi$ . In multiplying the two factors, the number of independent products are:

$$\mathcal{F}P_{\phi_Q\psi_Q} = \begin{cases} |\phi_Q||\psi_Q|/|S_{\phi_Q\psi_Q}| & \text{if } |S_{\phi_Q\psi_Q}| > 0; \\ |\phi_Q||\psi_Q| & \text{otherwise.} \end{cases}$$

Our approach parallelizes the above factor product using  $|\phi_Q|$  threads, with each thread performing  $\frac{|\psi_Q|}{|S_{\phi_Q\psi_Q}|}$  products if  $|S_{\phi_Q\psi_Q}| > 0$  otherwise  $|\psi_Q|$ . Analogously, the number of independent sums are:

$$\mathcal{F}S_{\psi'_Q\psi_Q} = \begin{cases} |\psi'_Q||\psi_Q|/|S_{\psi'_Q\psi_Q}| & \text{if } |S_{\psi'_Q\psi_Q}| > 0; \\ |\psi'_Q||\psi_Q| & \text{otherwise.} \end{cases}$$

For marginalization of a utility factor  $\psi_Q$  over a random variable  $Q$  in its domain, the number of independent maximizations are  $|\psi_Q|/|dom(Q)|$ , where  $dom(Q)$  gives the number of states of the variable,  $Q$ . We assign a thread to each independent maximization.

Let  $\mathbf{C}$ ,  $\mathbf{D}$  and  $\mathbf{U}$  denote the sets of decision, chance and utility variables respectively in the DID. We begin by establishing the time complexity of evaluating the sum-max-sum rule serially on a flat DID. Overall, this requires summing utility factors, whose complexity is  $\sum_{Q \in \mathbf{U}} \mathcal{F}S_{\psi'_Q\psi_Q} = O(|\mathbf{U}||\psi'_Q||\psi_Q|/|S_{\psi'_Q\psi_Q}|)$ ; performing as many factor products as there are chance variables, whose time complexity is  $\sum_{Q \in \mathbf{C}} \mathcal{F}P_{\phi_Q\psi_Q} = O(|\mathbf{C}||\phi_Q||\psi_Q|/|S_{\phi_Q\psi_Q}|)$ ; sum-marginalization of the chance variables in probability factors with complexity,  $O(|\mathbf{C}||\phi_Q|)$ ; and the maximization over the decision variables, whose complexity is  $O(|\mathbf{D}||\psi_{\mathbf{D}^*}|)$ . The total complexity for the serial computation is

$$O\left(\left(|\mathbf{U}|\frac{|\psi'_Q||\psi_Q|}{|S_{\psi'_Q\psi_Q}|}\right) + |\mathbf{C}||\phi_Q|\left(\frac{|\psi_Q|}{|S_{\phi_Q\psi_Q}|} + 1\right) + |\mathbf{D}||\psi_{\mathbf{D}^*}|\right).$$

Here,  $\psi'$  denotes an expected utility;  $\overline{S}_{\phi_Q\psi_Q}$ ,  $\overline{S}_{\psi_Q\psi_Q}$  are the smallest sets of shared variables between probability and utility factors respectively;  $\overline{D}$  is the decision variable with the largest utility factor to maximize over.

Each parallelized utility sum operation has a theoretical time of  $\mathcal{F}S_{\psi'_Q\psi_Q}|\psi_Q|$ ; the parallelized factor product requires a time of  $\mathcal{F}P_{\phi_Q\psi_Q}|\phi_Q|$ ; the parallelized sum-marginalization requires a time of  $|\phi_Q|/|dom(Q)|$ ; and the parallelized max-marginalization requires a time of  $|\psi_{\mathbf{D}^*}|/|dom(\mathbf{D})|$  units. Consequently, the total complexity for the parallel computation is:

$$O\left(\kappa + \left(|\mathbf{U}|\frac{|\psi_Q|}{|\overline{S}_{\psi'_Q\psi_Q}|}\right) + |\mathbf{C}|\left(\frac{|\psi_Q|}{|\overline{S}_{\phi_Q\psi_Q}|} + 1\right) + \frac{|\mathbf{D}||\psi_{\overline{D}}|}{|dom(\underline{D})|}\right)$$

where  $\underline{D}$  is the decision variable with the smallest domain size and  $\kappa$ , which is a function of the size of the network, is the total cost for kernel invocations and memory latency in the GPU.

**Theorem 2** (Speed up). *The speed up of evaluating the sum-max-sum rule for a flat DID with set,  $\mathbf{C}$ , of chance variables,  $\mathbf{D}$  of decision variables, and  $\mathbf{U}$  of utility variables is upper bounded by:*

$$\frac{\left(|\mathbf{U}|\frac{|\psi'_Q||\psi_Q|}{|\overline{S}_{\psi'_Q\psi_Q}|}\right) + |\mathbf{C}||\phi_Q|\left(\frac{|\psi_Q|}{|\overline{S}_{\phi_Q\psi_Q}|} + 1\right) + |\mathbf{D}||\psi_{\overline{D}}|}{\kappa + \left(|\mathbf{U}|\frac{|\psi_Q|}{|\overline{S}_{\psi'_Q\psi_Q}|}\right) + |\mathbf{C}|\left(\frac{|\psi_Q|}{|\overline{S}_{\phi_Q\psi_Q}|} + 1\right) + \frac{|\mathbf{D}||\psi_{\overline{D}}|}{|dom(\underline{D})|}}$$

where  $\psi'$  denotes an expected utility;  $\overline{S}_{\phi_Q\psi_Q}$ ,  $\overline{S}_{\psi_Q\psi_Q}$  are the smallest sets of shared variables between probability and utility factors respectively;  $\overline{D}$  is the decision variable with the largest utility factor to maximize over;  $\underline{D}$  is the decision variable with the smallest domain size and  $\kappa$ , which is a function of the size of the network, is the total cost for kernel invocations and memory latency in the GPU.

## 7 EXPERIMENTS

In this section we empirically evaluate the performance and scalability of Parallelized I-DID Exact on different networks against its serial implementation I-DID Exact. Experiments were performed on a desktop with Intel CPU (3.10GHz), 16GB RAM and a NVIDIA Geforce GTX480 graphics card with 480 cores, 1.5GB global memory and 64KB of shared memory for each SM.

Besides the tiger problem ( $|S|=2$ ,  $|A_i|=|A_j|=3$ ,  $|\Omega_i|=6$  and  $|\Omega_j|=2$ ), we also evaluated the proposed approach on a larger problem domain: the two-agent

unmanned aerial vehicle (UAV) interception problem ( $|S_i|=25$ ,  $|S_j|=9$ ,  $|A_i|=|A_j|=5$ ,  $|\Omega_i|=|\Omega_j|=5$ ). In this problem, there is a UAV and a fugitive with noisy sensors and unreliable actuators locating in a  $3 \times 3$  grid. The fugitive  $j$  plans to reach the safe house while avoiding detection by the hostile UAV  $i$  (Zeng and Doshi, 2012).

## 7.1 PERFORMANCE EVALUATION

For the Tiger problem, different numbers of (10, 50, and 100) level 0 DIDs with the number of planning horizons from 6 to 9 are solved and used to expand the level 1 I-DIDs of 3 to 5 horizons. The average factor sizes increases along with the number of horizons. The mean speed up ranges between 6 and slightly greater than 10, with I-DIDs of longer horizon demonstrating greater speed up in their solution. Due to the complexity of the UAV domain and limited global memory, the current implementation solves the problem optimally up to horizon 3. However, parallelized I-DID Exact still provides promising speedups. Problems with larger factors, which can contain more common variables, show greater speedups.

All experiment results are summarized in Tables 1 and 2. The I-DIDs for the different problem domains unrolled to different look ahead ( $T_1$ ) with different number of level 0 models (the column  $|M_j|$ ) at different look aheads ( $T_0$ ) were used to evaluate the performance of the proposed algorithm. The average sizes of factors processed during variable elimination, including probability and utility factors, of level 0 and level 1 models are listed in columns titled by  $\text{Mean}(|\phi|)$  and  $\text{Mean}(|\psi|)$ . Columns labeled by CPU and GPU contain the total running times, which includes the time for solving level 0 models, expansion, and solving the resulting level 1 model. The speedup is indicated in the last column titled with *Speedup*.

As suggested in Theorem 2, the theoretical speedup, as lower bounds, for these two domains are  $70/(\kappa + 22)$  and  $450/(\kappa + 28)$ , respectively, where  $\kappa$  is the total cost for kernel invocations and memory latency in the GPU. As the tiger problem is a small domain, the cost of data transmission is negligible, and the lower bound can be seen as approximately 4. However for the larger UAV problem, a comparison with the reported empirical speedup shows that  $\kappa$  is not negligible.

Fig. 7 shows the speedup for the Tiger and the UAV domain with different problem sizes. Overall, the speed up in planning optimally increases as the sizes of the level 1 and level 0 planning problems increase. Varying the number of candidate models (DIDs) ascribed to the other agent did not signifi-

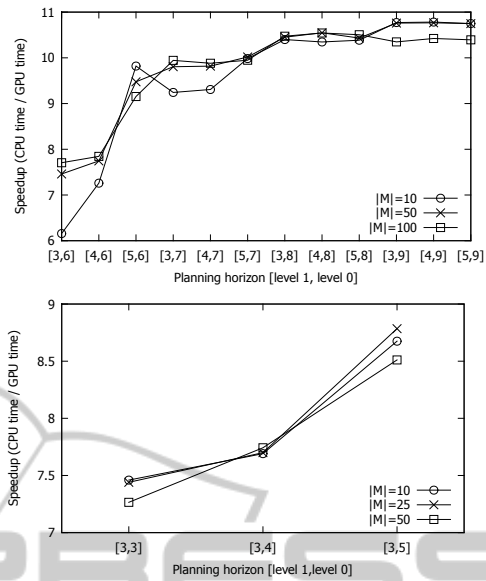


Figure 8: The speedup for the multiagent tiger problem and the UAV problem given different amount of level 0 models and number of decision horizons.

cantly impact the speed ups. This is expected as the lower-level models are solved sequentially. Parallelization of their solutions seems to be an obvious avenue of future work, but is deceptively challenging.

## 7.2 Optimizing Thread Block Size

By parallelizing the computation on the GPU, we observed around an order of magnitude speedup through the performed experiments. As computation tasks are organized as a set of thread blocks and executed on SMs, the number of thread blocks determines the overall performance. Generally speaking, more thread blocks will increase the degree of parallelization with higher synchronization cost. Automatically calculating the optimal thread-block sizes (Sano et al., 2014), which is domain dependent, is beneficial but computationally expensive. The expense may be amortized over multiple runs. But, because we solve I-DIDs just once for a domain, this expense cannot be amortized and significantly adds to the run time. As a trade-off, we empirically search for a block size that optimizes the solution for many problem domains following the CUDA optimization heuristics.

We evaluated the performance of Parallelized I-DID Exact as the number of threads in each block is increased from 64 to 640, on a level 1 I-DID of horizon 3 and 10 lower-level DIDs as candidate models. The impact of different block sizes on run time is shown in Fig. 9. As observed, the block size of 512 gives the best performance in terms of running

Table 1: Run times, factor sizes and speed ups for the multiagent tiger problem.  $|M_j|$  denotes the number of level 0 models. **Mean**  $|\phi|$  and **Mean**  $|\psi|$  are the average sizes of probability and utility factors in the models, respectively. Columns titled by CPU and GPU denote the running times for different implementations. The speedups are listed in the last column.

| $ M_j $ | Level 1 |                      |                      | Level 0 |                      |                      | Time (seconds) |       |             |
|---------|---------|----------------------|----------------------|---------|----------------------|----------------------|----------------|-------|-------------|
|         | $T_1$   | <b>Mean</b> $ \phi $ | <b>Mean</b> $ \psi $ | $T_0$   | <b>Mean</b> $ \phi $ | <b>Mean</b> $ \psi $ | CPU            | GPU   | Speedup     |
| 10      | 3       | 1959                 | 2237                 | 6       | 2192                 | 1703                 | 3.14           | 0.51  | <b>6.2</b>  |
|         |         |                      |                      | 7       | 11126                | 8620                 | 17.8           | 1.93  | <b>9.2</b>  |
|         |         |                      |                      | 8       | 58835                | 45556                | 106            | 10.2  | <b>10.4</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 644            | 60.0  | <b>10.8</b> |
|         | 4       | 38376                | 44998                | 6       | 2192                 | 1703                 | 5.59           | 0.77  | <b>7.3</b>  |
|         |         |                      |                      | 7       | 11126                | 8620                 | 20.3           | 2.18  | <b>9.3</b>  |
|         |         |                      |                      | 8       | 58835                | 45556                | 108            | 10.5  | <b>10.3</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 647            | 60.0  | <b>10.8</b> |
|         | 5       | 600493               | 655141               | 6       | 2192                 | 1703                 | 50.0           | 5.09  | <b>9.8</b>  |
|         |         |                      |                      | 7       | 11126                | 8620                 | 64.7           | 6.48  | <b>10.0</b> |
|         |         |                      |                      | 8       | 58835                | 45556                | 153            | 14.7  | <b>10.4</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 691            | 64.3  | <b>10.7</b> |
| 50      | 3       | 5449                 | 5307                 | 6       | 2192                 | 1703                 | 13.7           | 1.83  | <b>7.5</b>  |
|         |         |                      |                      | 7       | 11126                | 8620                 | 80.5           | 8.21  | <b>9.8</b>  |
|         |         |                      |                      | 8       | 58835                | 45556                | 481            | 46.0  | <b>10.5</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 2930           | 272   | <b>10.8</b> |
|         | 4       | 63225                | 65249                | 6       | 2192                 | 1703                 | 16.1           | 2.08  | <b>7.7</b>  |
|         |         |                      |                      | 7       | 11126                | 8620                 | 83.0           | 8.45  | <b>9.8</b>  |
|         |         |                      |                      | 8       | 58835                | 45556                | 484            | 45.9  | <b>10.5</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 2931           | 272   | <b>10.7</b> |
|         | 5       | 672794               | 683530               | 6       | 2192                 | 1703                 | 60.5           | 6.44  | <b>9.4</b>  |
|         |         | 879830               | 910980               | 7       | 11126                | 8620                 | 127            | 12.7  | <b>10.0</b> |
|         |         |                      |                      | 8       | 58835                | 45556                | 528            | 50.6  | <b>10.4</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 2972           | 277   | <b>10.7</b> |
| 100     | 3       | 5546                 | 5322                 | 6       | 2192                 | 1703                 | 27.4           | 3.56  | <b>7.7</b>  |
|         |         |                      |                      | 7       | 11126                | 8620                 | 162.5          | 16.3  | <b>9.9</b>  |
|         |         |                      |                      | 8       | 58835                | 45556                | 971            | 92.8  | <b>10.4</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 5937           | 573   | <b>10.3</b> |
|         | 4       | 63294                | 65260                | 6       | 2192                 | 1703                 | 29.9           | 3.81  | <b>7.8</b>  |
|         |         |                      |                      | 7       | 11126                | 8620                 | 164.9          | 16.7  | <b>9.8</b>  |
|         |         |                      |                      | 8       | 58835                | 45556                | 974            | 92.4  | <b>10.5</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 5937           | 569.6 | <b>10.7</b> |
|         | 5       | 672848               | 683538               | 6       | 2192                 | 1703                 | 74.3           | 8.11  | <b>9.2</b>  |
|         |         | 879884               | 910989               | 7       | 11126                | 8620                 | 209            | 21.0  | <b>9.9</b>  |
|         |         |                      |                      | 8       | 58835                | 45556                | 1018           | 96.8  | <b>10.5</b> |
|         |         |                      |                      | 9       | 306284               | 237130               | 5975           | 575   | <b>10.4</b> |

Table 2: Run times, factor sizes and speed ups for the multiagent UAV problem. Columns have similar meanings.

| $ M_j $ | Level 1 |                      |                      | Level 0 |                      |                      | Time (seconds) |      |            |
|---------|---------|----------------------|----------------------|---------|----------------------|----------------------|----------------|------|------------|
|         | $T_1$   | <b>Mean</b> $ \phi $ | <b>Mean</b> $ \psi $ | $T_0$   | <b>Mean</b> $ \phi $ | <b>Mean</b> $ \psi $ | CPU            | GPU  | Speedup    |
| 10      | 3       | 104223               | 75120                | 3       | 1235                 | 1029                 | 16.56          | 2.22 | <b>7.5</b> |
|         |         |                      |                      | 4       | 20237                | 9467                 | 24.38          | 3.17 | <b>7.7</b> |
|         |         |                      |                      | 5       | 392043               | 170405               | 239            | 27.6 | <b>8.7</b> |
| 25      | 3       | 106410               | 75270                | 3       | 1235                 | 1029                 | 16.9           | 2.27 | <b>7.4</b> |
|         |         |                      |                      | 4       | 20237                | 9467                 | 32.6           | 4.23 | <b>7.7</b> |
|         |         |                      |                      | 5       | 392043               | 170405               | 462.4          | 55.6 | <b>8.8</b> |
| 50      | 3       | 209573               | 117520               | 3       | 1235                 | 1029                 | 17.51          | 2.41 | <b>7.3</b> |
|         |         | 212260               | 117695               | 4       | 20237                | 9467                 | 46.61          | 6.02 | <b>7.7</b> |
|         |         | 153348               | 81195                | 5       | 392043               | 170405               | 845.1          | 99.3 | <b>8.5</b> |

time. The upside is that as more threads are involved in the computation there are less iterations of fetching global memory loads to shared memory. In contrast, the degradation in performance is expected because spawning more threads per block limits the number

of blocks that can be scheduled to run concurrently because of limited resources, hence, the observed fall in performance.

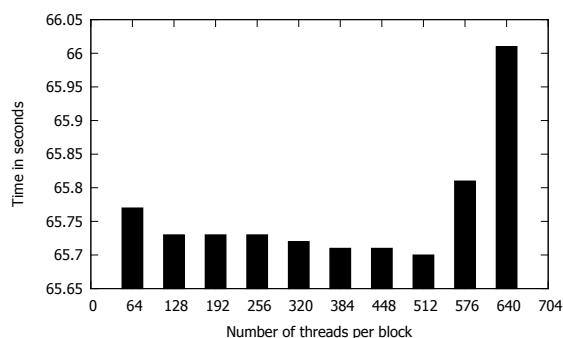


Figure 9: The running time of the multiagent tiger problem given different GPU's thread block sizes.

## 8 CONCLUSION

We presented a method for optimal planning in multi-agent settings under uncertainty that utilizes the parallelism provided by a heterogeneous CPU-GPU computing architecture. We focused on the interactive dynamic influence diagrams, which are probabilistic graphical models whose solution involves transforming the I-DID into a flat DID and computing the policy with the maximum expected utility. Operations involving probability and utility factors during variable elimination are parallelized on GPUs. We demonstrate speed ups close to an order of magnitude on multiple problem domains and run times that are less than 17 minutes for large numbers of models and long horizons. To the best of our knowledge, these are the fastest run times reported so far for exactly solving I-DIDs and other related frameworks such as I-POMDPs for multiagent planning, and represent a significant step forward in making these complex frameworks practical.

As aforementioned, lower level models can be DIDs or I-DIDs with different initial beliefs. These candidate models are differing hypotheses of the other agent's behavior, and therefore may be solved independently in parallel. However, as solving I-DIDs requires large amount of memory, we may not solve these in parallel on a single GPU. Nevertheless, modern computing platforms may contain two or more GPU units linked together and programmable using CUDA.<sup>2</sup> Furthermore, multiple networked machines with GPUs may be utilized using CUDA-MPI. However, as the factor operation is not computational intensive, whether the saving from the parallel computation on the GPU side can compensate the cost of transporting data between CPU and GPU is still an

<sup>2</sup>NVIDIA promotes having multiple GPU units managed by its scalable link interface.

open question. Comparisons based on different types of GPUs will be our immediate future work as well.

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